



DIM250PHM33-TL000

Half Bridge IGBT Module

DS6116-1 July 2013 (LN30665)

FEATURES

- Low V_{CE(sat)} Device
- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- High Current Density Enhanced DMOS SPT
- Isolated AISiC Base with AIN Substrates

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Auxiliaries
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM250PHM33-TL000 is a Low $V_{CE(sat)}$ half bridge 3300V, soft punch through n-channel enhancement mode, insulated gate bipolar transistor (IGBT) chopper module configured with the lower arm of the bridge controlled. The IGBT has a wide reverse bias safe operating area (RBSOA). This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM250PHM33-TL000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V _{CES}		3300V
V _{CE(sat)}	* (typ)	2.0V
lc	(max)	250A
I _{C(PK)}	(max)	500A

* Measured at the auxiliary terminals

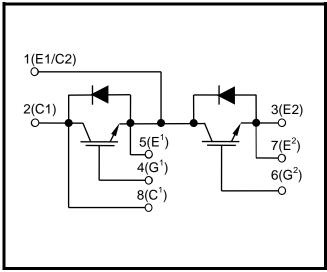
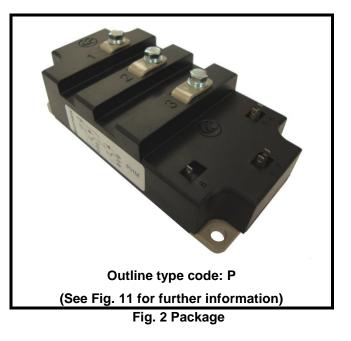


Fig. 1 Circuit configuration



ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	V _{GE} = 0V	3300	V
V _{GES}	Gate-emitter voltage		±20	V
Ι _C	Continuous collector current	T _{case} = 115°C	250	А
I _{C(PK)}	Peak collector current	1ms, $T_{case} = 140^{\circ}C$	500	А
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_j = 150^{\circ}C$	2.6	kW
l ² t	Diode I ² t value	$V_R = 0, t_p = 10ms, T_j = 125^{o}C$	20	kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	6000	V
Q _{PD}	Partial discharge – per module	IEC1287, $V_1 = 3500V$, $V_2 = 2600V$, 50Hz RMS	10	рС

THERMAL AND MECHANICAL RATINGS

Internal insulation material:	AIN
Baseplate material:	AISiC
Creepage distance:	33mm
Clearance:	20mm
CTI (Comparative Tracking Index):	>600

Symbol	Parameter	Test Conditions	Min	Тур.	Мах	Units
R _{th(j-c)}	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	48	°C/kW
R _{th(j-c)}	Thermal resistance – Diode	Continuous dissipation - junction to case	-	-	96	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	16	°C/kW
Tj	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	150	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M5	-	-	4	Nm

ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
		$V_{GE} = 0V, V_{CE} = V_{CES}$			1	mA
I _{CES}	Collector cut-off current	$V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125^{\circ}C$			15	mA
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 150^{\circ}C$			25	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			1	μA
$V_{\text{GE(TH)}}$	Gate threshold voltage	I_{C} = 20mA, V_{GE} = V_{CE}		5.7		V
		V _{GE} = 15V, I _C = 250A		2.0		V
$V_{CE(sat)}^{\dagger}$	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 250A, T _j = 125°C		2.6		V
		$V_{GE} = 15V, I_{C} = 250A, T_{j} = 150^{\circ}C$		2.8		V
I _F	Diode forward current	DC		250		А
I _{FM}	Diode maximum forward current	t _p = 1ms		500		А
	Diode forward voltage	I _F = 250A		2.4		V
V_{F}^{\dagger}		I _F = 250A, T _j = 125°C		2.5		V
		I _F = 250A, T _j = 150°C		2.4		V
C _{ies}	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		45		nF
Qg	Gate charge	±15V Including external C _{ge}		5		μC
C _{res}	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		1		nF
L _M	Module inductance			40		nH
R _{INT}	Internal transistor resistance			500		μΩ
SC _{Data}	Short circuit current, I _{SC}	$T_{j} = 150^{\circ}C, V_{CC} = 2500V$ $t_{p} \le 10\mu s, V_{GE} \le 15V$ $V_{CE (max)} = V_{CES} - L^{*}x \ dI/dt$ IEC 60747-9		950		A

Note:

 † Measured at the auxiliary terminals * L is the circuit inductance + L_{M}

ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	I _C = 250A		2700		ns
t _f	Fall time	$V_{GE} = \pm 15V$		610		ns
E _{OFF}	Turn-off energy loss	$V_{CE} = 1800V$		650		mJ
t _{d(on)}	Turn-on delay time	$R_{g(ON)} = 10\Omega$ $R_{g(OFF)} = 10\Omega$		960		ns
t _r	Rise time	$C_{GE} = 56nF$		430		ns
E _{ON}	Turn-on energy loss	L _s ~ 150nH		400		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 250A		140		μC
I _{rr}	Diode reverse recovery current	$V_{CE} = 1800V$		150		А
E _{rec}	Diode reverse recovery energy	dI _F /dt = 700A/µs		170		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Мах	Units
t _{d(off)}	Turn-off delay time	I _C = 250A		2750		ns
t _f	Fall time	$V_{GE} = \pm 15V$		590		ns
E _{OFF}	Turn-off energy loss	$V_{CE} = 1800V$		680		mJ
t _{d(on)}	Turn-on delay time	$R_{g(ON)} = 10\Omega$ $R_{g(OFF)} = 10\Omega$		1000		ns
tr	Rise time	$C_{GE} = 56nF$		460		ns
E _{ON}	Turn-on energy loss	L _s ~ 150nH		520		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 250A		230		μC
I _{rr}	Diode reverse recovery current	$V_{CE} = 1800V$		190		А
E _{rec}	Diode reverse recovery energy	dI _F /dt = 700A/µs		280		mJ

T_{case} = 150°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Мах	Units
t _{d(off)}	Turn-off delay time	I _C = 250A		2760		ns
t _f	Fall time	$V_{GE} = \pm 15V$		590		ns
E _{OFF}	Turn-off energy loss	$V_{CE} = 1800V$		750		mJ
t _{d(on)}	Turn-on delay time	$R_{g(ON)} = 10\Omega$ $R_{g(OFF)} = 10\Omega$		940		ns
t _r	Rise time	$C_{GE} = 56nF$		460		ns
E _{ON}	Turn-on energy loss	L _s ~ 150nH		550		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 250A		270		μC
١ _{rr}	Diode reverse recovery current	$V_{CE} = 1800V$		200		А
E _{rec}	Diode reverse recovery energy	dI _F /dt = 700A/µs		330		mJ

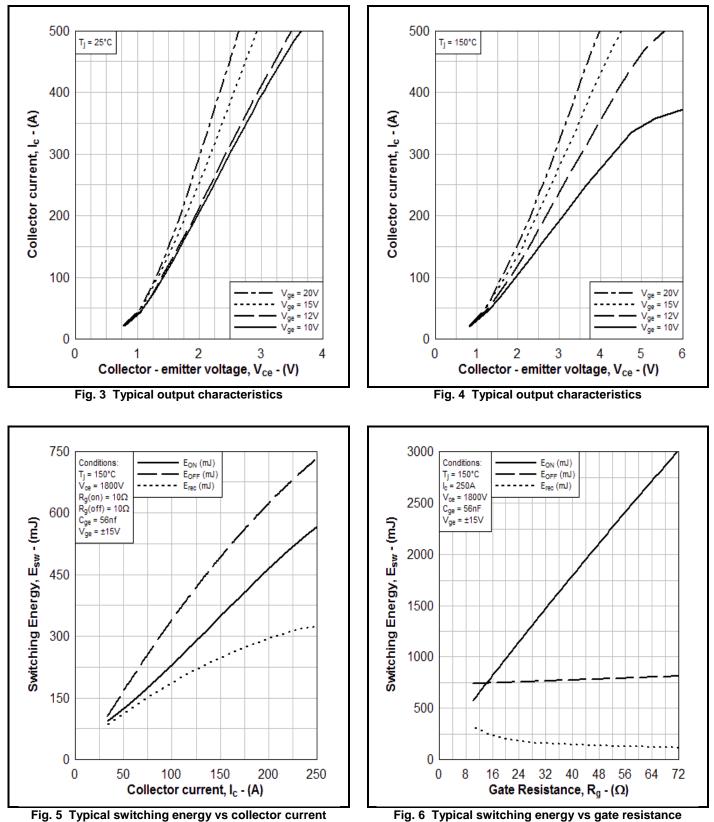


Fig. 6 Typical switching energy vs gate resistance

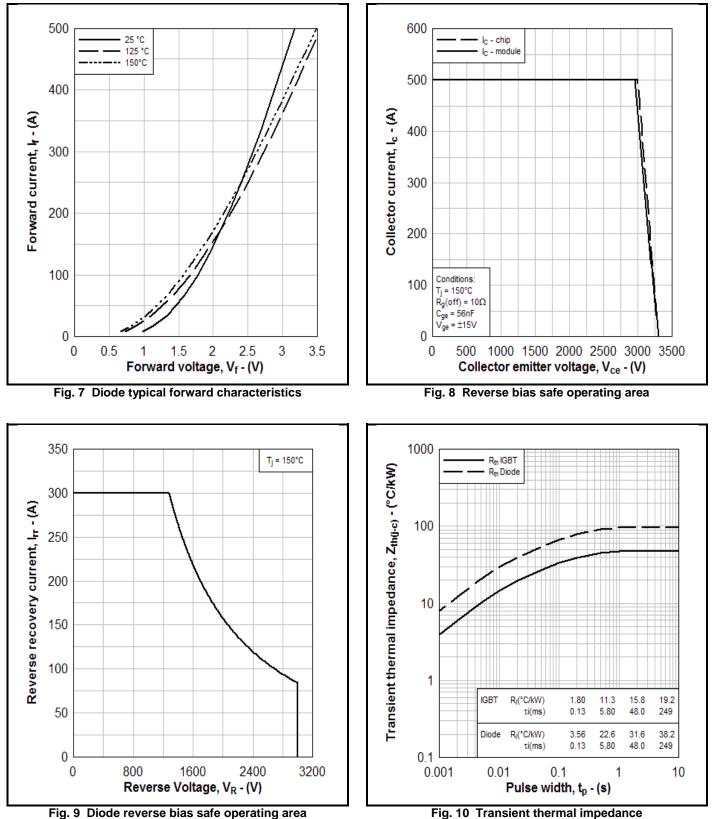
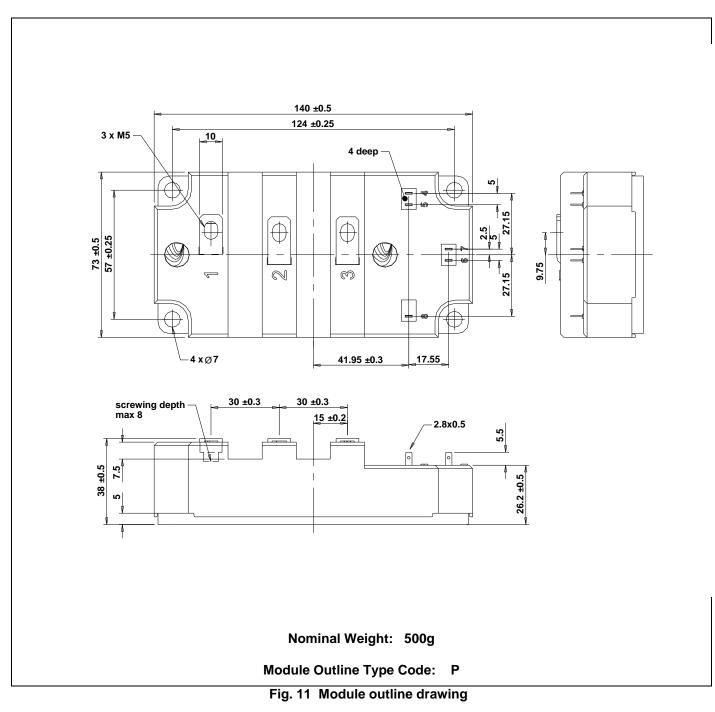


Fig. 9 Diode reverse bias safe operating area

PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise. **DO NOT SCALE.**



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